

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1572	comparator and integrated adj circuit and (switch or multiplexor) and reference adj voltage and storage and test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 09:40
L2	1048	l1 and error	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 09:41
L3	864	l1 and error and internal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 09:43
L4	142	l1 and error and internal adj voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 09:45